

REMARKS

Except as discussed below, Applicants are amending the application in order to clarify the meaning of the pending claims.

Applicants will address each of the Examiner's rejections in the order in which they appear in the Final Rejection.

Claim Rejections - 35 USC §102

Claims 1-18

In the Final Rejection, the Examiner continues to reject Claims 1-18 under 35 USC §102(e) as being unpatentable over US publication no. 2002/0044124 (Yamazaki et al.). This rejection is respectfully traversed.

While Applicants traverse this rejection, in order to advance the prosecution of this application, Applicants are canceling independent Claim 1 without prejudice or disclaimer, and amending independent Claims 2, 4 and 5. In particular, Claim 2 has been amended to recite "wherein pixels of the plurality of pixels are arranged in each pixel column and two or more data lines extend in each of the pixels to simultaneously supply video signals to the pixels through the data lines." Claims 4 and 5 have been amended to recite "a plurality of pixels arranged in a matrix pattern in a pixel portion, the matrix pattern having a plurality of pixel columns in which the pixels are arranged in a column direction." These features are disclosed, for example, in Figs. 2A and 5 of the present application. For example, Fig. 2A shows a plurality of pixels (E, E-1, etc.) arranged in a matrix pattern with the matrix having a plurality of pixel columns (the column with E, E-1, etc. being an example) wherein each pixel column has a plurality of pixels (e.g. E, E-1) arranged in column direction. Further, two or more data lines (SA1 - SD1) extend

to each pixel in a pixel column. See also e.g. Fig. 8. Fig. 5 is an example of one pixel column showing the plurality of pixels and data lines extending thereto.

In the Final Rejection, the Examiner contends that Yamazaki discloses column 101 as having “four or more data lines (not shown) in the pixel portion (103)” in Fig. 1 and “column (1802) has four or more data lines in the pixel portion (1801)” in Fig. 18. Applicants respectfully disagree.

In particular, column 101 and column 1802 are each a “source signal line driver circuit” and are not a pixel column with a plurality of pixels or a pixel column with a plurality of pixels arranged in a column direction, as in independent Claims 2, 4, and 5 of the present application.

Therefore, Yamazaki does not disclose or suggest the device of independent Claims 2, 4 or 5, and therefore, Claims 2-18 are patentable thereover. Accordingly, it is respectfully requested that this rejection be withdrawn.

Claims 19-20

The Examiner also continues to reject Claims 19-20 under 35 USC §102(e) as being unpatentable over US publication no. 2001/0035863 (Kimura et al.). This rejection is also respectfully traversed.

While this rejection is also respectfully traversed, in order to advance the prosecution of this application, Applicants are amending independent Claim 19 to recite “a plurality of pixels arranged in a matrix pattern in a pixel portion, the matrix pattern having a plurality of pixel columns in which the pixels are arranged in a column direction.” Applicants respectfully submit that this feature is not disclosed or suggested by Kimura.

Therefore, Kimura does not disclose or suggest the method of independent Claims 19 and 20, and therefore, these claims are patentable thereover. Accordingly, it is respectfully requested that this rejection be withdrawn.

New Claims

Applicants are also adding new Claims 21-34 herewith to further claim the present invention. Please charge our deposit account 50/1039 for any fee due for these new claims.

Conclusion

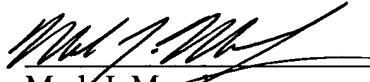
It is respectfully submitted that the present application is a condition for allowance and should be allowed.

If any further fee is due for this amendment, please charge our deposit account 50/1039.

Favorable reconsideration is earnestly solicited.

Respectfully submitted,

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Mark J. Murphy
Registration No. 34,225

COOK, ALEX, McFARRON, MANZO,
CUMMINGS & MEHLER, LTD.
200 West Adams Street
Suite 2850
Chicago, Illinois 60606
(312) 236-8500

Customer No. 000026568